> C

: SWm SW2Tx CONTROL SECTION SW1116 B.P.F SWITCH CONTROL CIRCUIT ga, TxIN Txc1 T_{xC2} Tx ° RECEIVING ERROR
DETECTING CIRCUIT COMMUNICATION CONTROLLER φ īύ F I G. ROM RAM CPU

ω-

F I G. 2

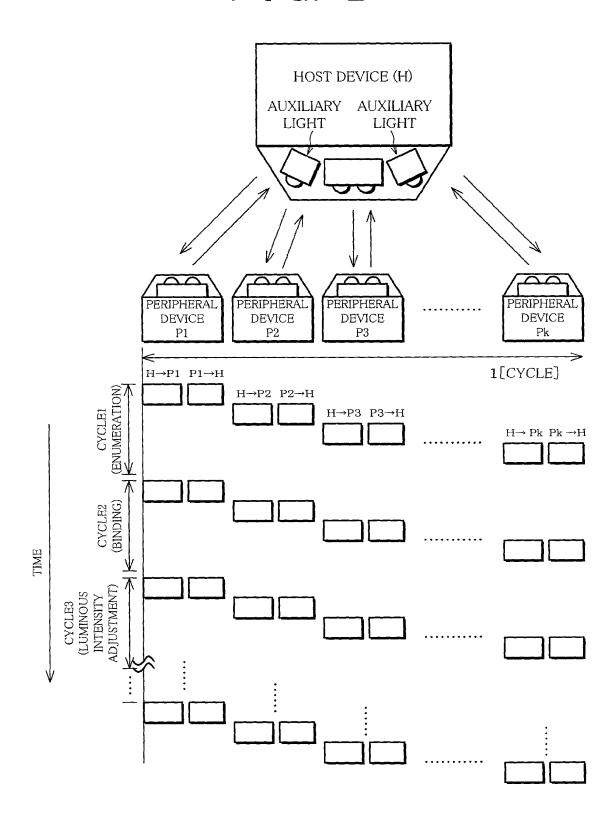


FIG. 3

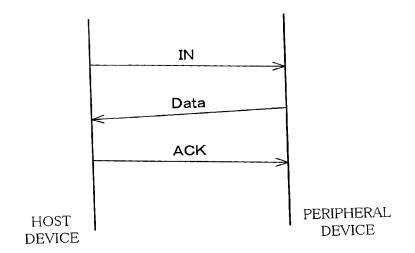
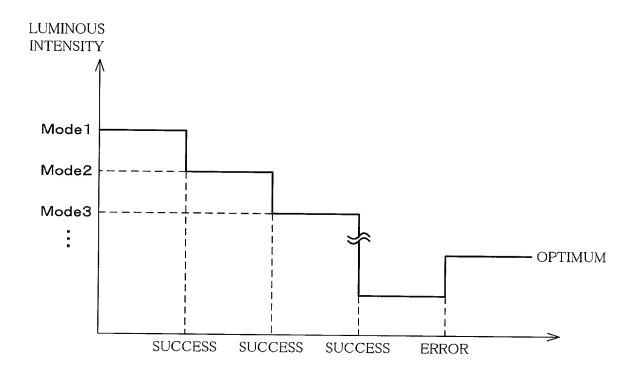
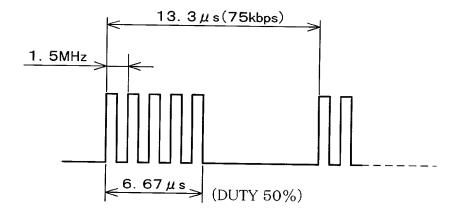


FIG. 4



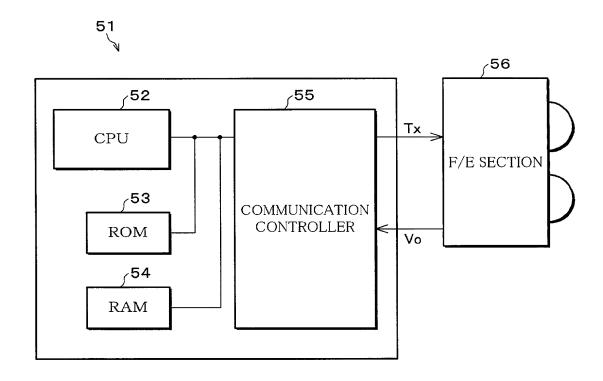
F I G. 5



F I G. 6

STO
CRC
MAC frame
STA
PRE
AGC

F I G. 7



3 1 4 6

FIG. 8

